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	Applicants: Hojo, et	al.
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## **U.S. PATENT DOCUMENTS**

Examiner Initial			Name	Class Sub- class		Filing date if appropriate	
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## FOREIGN PATENT DOCUMENTS

	Document	Date	Country	Class	Sub-	Translation	
	number				class	YES	NO
(6-	-2000-182394	6-30-2000	Japan	<u>.</u>			X

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

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if not in conformance and not considered. Include copy of this form with next communication to the applicant.

Ouellette, et al, "Shared Fuse Macro for Multiple Embedded Memory Devices with Redundancy," IBM, Microelectronics Division, ASIC Development, Essex Junction, VT 05452, US, 4 pages, 2001 IEEE.

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